



- ▶ Very low RMS jitter
- ▶ Short Lead time
- ▶ Pb Free/RoHS2 Compliant
- ▶ MSL 1
- ▶ Peak solder temp +260°C (10 sec)

# ECSpressCON™ ECXV-L LVDS VCXO

ECXV-L2 (2.5V) and ECXV-L3 (3.3V) low jitter, low current Frequency Configurable SMD LVDS Voltage Controlled Crystal Oscillators (VCXO).

## OPERATING CONDITIONS / ELECTRICAL CHARACTERISTICS

PARAMETERS	CONDITIONS	ECXV-L2 (+2.5V)			ECXV-L3 (+3.3V)			UNITS
		MIN	TYP	MAX	MIN	TYP	MAX	
Frequency Range		10.000		1500.00	10.000		1500.00	MHz
Supply Voltage		+2.375	+2.5	+2.625	+2.97	+3.3	+3.63	VDC
Voltage Control		+0.2	+1.25	+2.3	+0.3	+1.65	+3.0	VDC
Frequency Pulling Range (Positive Transfer)	7X5 & 5X3.2 Pkg	±100			±100			ppm
	3.2X2.5 & 2.5X2 Pkg	±50			±50			ppm
Frequency Stability *	Option A			± 100			± 100	ppm
	Option B			± 50			± 50	ppm
	Option C			± 25			± 25	ppm
	Option D			± 20			± 20	ppm
Input Current	10.0 to 250.0 MHz			18			20	mA
	250.1 to 500.0 MHz			21			22	mA
	500.1 to 1500 MHz			26			28	mA
Output Symmetry	@ 50%Vcc level			48/52			48/52	%
Aging	@ +25°C (first year)			±2			±2	ppm
Rise and Fall Times	10% Vdd to 90% level	150		250	150		250	ps
"0" level	VOL	0.9	1.1		0.9	1.1		VDC
"1" level	VOH		1.4	1.6		1.4	1.6	VDC
Output Load	Differential							
Output Enable	Pin 2 **	0.7%			0.7%			Vdd
Output Disable	Pin 2			0.3%			0.3%	Vdd
Output Enable Time				200			200	ns
Output Disable Time				50			50	ns
Phase Jitter, rms	12 KHz to 20 MHz		1.0			1.0		pS
Operating Temperature (Specified in P/N)	Standard	-10		+70	-10		+70	°C
	Extended (P Option)	-40		+105	-40		+105	°C
Storage Temperature		-55		+125	-55		+125	°C
Moisture Sensitivity Level		1						
Termination Finish		Au						
ESD Sensitivity	Human Body Model	3 kV Max.						

### Part Number Guide

Series	Voltage	Package	Stability	Pull Range (***)	Operating Temp	Frequency
ECXV-L (LVDS Output)	2 = +2.5V 3 = +3.3V	2 = 2.5 x 2 mm 3 = 3.2 x 2.5 mm 5 = 5 x 3.2 mm 7 = 7 x 5 mm	A = ±100 ppm B = ±50 ppm C = ±25 ppm D = ±20 ppm	1 = ±50 ppm 2 = ±90 ppm 3 = ±100 ppm	L = -10 ~ +70°C M = -20 ~ +70°C N = -40 ~ +85°C P = -40 ~ +105°C	- Customer Specified

## Example ECXV-L35B3N-156.250

\* Note: Inclusive of 25°C tolerance, operating temperature, input voltage change, load change, shock and vibration.

\*\* Note: Internal pull-up resistor active output if pin 2 is left open.

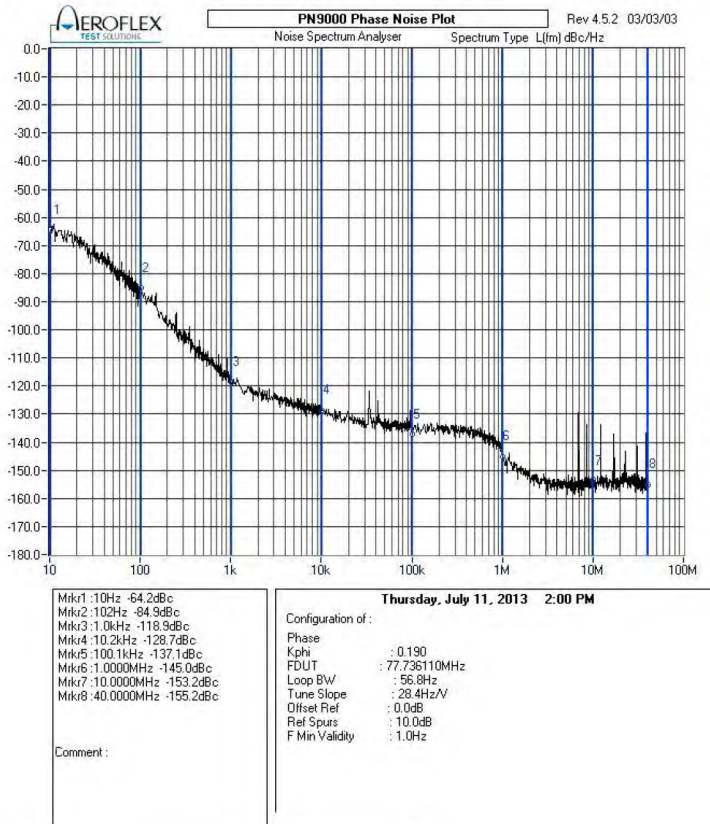
\*\*\* Pull Range is package dependent.



### Phase Noise and Jitter Data (typical)

SSB Phase Noise Data (dBc/Hz typical)	Frequency (offset)	77.760	122.880	125.000	156.250	212.5	491.520	622.080	1000	1250
	10 Hz	-64	-68	-63	-55	-62	-61	-48	-52	-42
	100 Hz	-84	-99	-94	-85	-93	-86	-85	-82	-81
	1 KHz	-118	-113	-113	-109	-105	-100	-101	-93	-93
	10 KHz	-128	-119	-118	-116	-113	-105	-102	-97	-96
	100 KHz	-137	-120	-119	-118	-115	-105	-103	-97	-97
	1 MHz	-145	-140	-137	-139	-135	-126	-124	-116	-119
	5 MHz	-152	-142	-146	-146	-143	-137	-133	-127	-129
Phase Jitter pS 12 KHz ~ 20 MHz, RMS		0.9	0.8	1.1	0.9	1.0	1.1	1.2	1.5	1.1

### Phase Noise Plot of ECXV-L35B3N-77.760 (typical)



Package Data	
Item	Description
Lid	Metal
Base	Ceramic
Plating	Gold/Nickel Surface/Under

## ECXV-L LVDS VCXO

### Dimensions (mm)

7 = 7x5 Package

5 = 5x3.2 Package

3 = 3.2x2.5 Package

2 = 2.5x2 Package

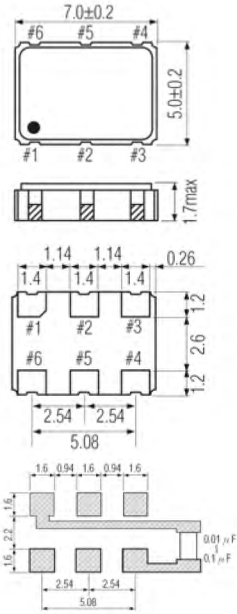


Figure 1) Top, Side, Bottom & Land

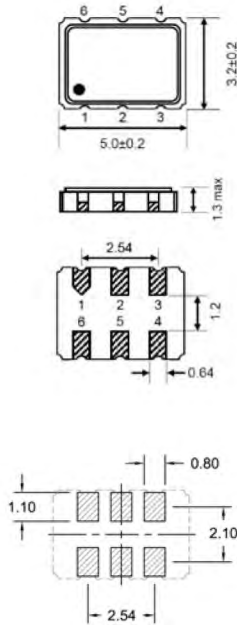


Figure 2) Top, Side, Bottom & Land

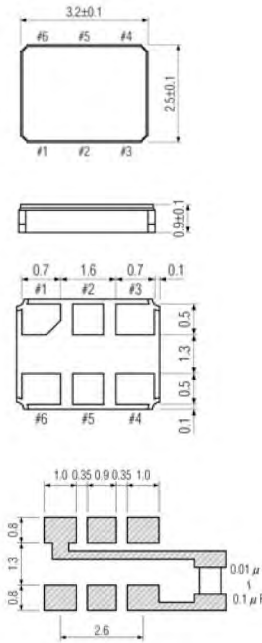


Figure 3) Top, Side, Bottom & Land

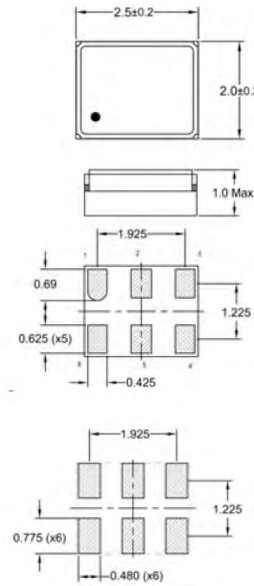
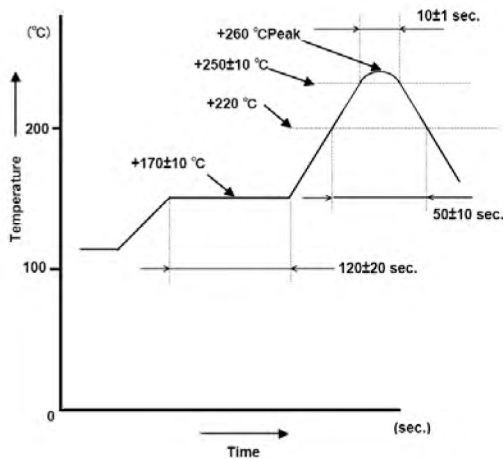


Figure 4) Top, Side, Bottom & Land

### Suggested Reflow Profile



Pin Connections	
Pin #	Function
1	Control Voltage
2	OE: High Enable
3	Ground
4	LVDS Differential
5	Complementary Output
6	Supply Voltage